



Department of Electronics and Communication Engineering (2024 - 2025)

5th A Div

With Effect from 7/10/2024



Gogte Institute of Technology, Udyambag, Belgaum-590 008

AF-31

	1 10:00-10:55	2 10:55 - 11:50	B1 11:50-12:10	3 12:10-1:05	B2 1:05 - 2:00	4 2:00-2:55	5 3:00-5:00	
Mo	(10am-1pm) VLSI Lab-A1 (SMK/GN/MM) DSP Lab-A2 (SM/ASA/RBK)		BREAK		LUNCH	(2pm-5pm) VLSI Lab-A2 (SNB/PUK/GN) DSP Lab-A1 (ABG/UUD/VKA)		
Tu	PE	VLSI SMK		VLSI Lab-A2 (SNB/PUK)		DSP ASA	EVS (3pm-5pm)	
We	RMIPR SS	M&E VKS		DSP ASA		VLSI SMK	M&E VKS	
Th	PE	DSP ASA		VLSI Lab-A1 (SMK/MM)		BIZOTIC (2-4pm)		Project Work
Fr	PE	M&E VKS		VLSI SMK		RMIPR SS	Project Work (3pm-5pm)	
Sa	AEE/IEEE/ISA			Remedial Class		Project Work	Remedial Class	

PE:AS-AVD,OS-VKS,ESD-AAG,DVD-ABG

Timetable Coordinator

HOD, E&C



Department of Electronics and Communication Engineering (2024 - 2025)

5th B Div

With Effect from 7/10/2024



Gogte Institute of Technology, Udyambag, Belgaum-590 008

AF-34

	1 10:00-10:55	2 10:55 - 11:50	B1 11:50-12:10	3 12:10-1:05	B2 1:05 - 2:00	4 2:00-2:55	5 3:00-5:00	
Mo	M&E AVD	DSP VKA	BREAK	RMIPR SM	LUNCH	EVS (2pm-4pm) Project Work		
Tu	PE	VLSI GN		DSP VKA		M&E AVD	Project Work (3pm-5pm)	
We	(10am-1pm) VLSI Lab-B1 (SMK/GN/MM) DSP Lab-B2 (ABG/RBK/VKA)			(2pm-5pm) VLSI Lab-B2 (SNB/PUK/GN) DSP Lab-B1 (RBK/VKA/ASA)				
Th	PE	DSP VKA		VLSI GN		RMIPR SM	(3pm-4pm) VLSI Lab-B2 (SMK/PUK)	
Fr	PE	VLSI GN		VLSI Lab-B1 (SNB/MM)		M&E AVD	BIZOTIC (3-5pm)	
Sa	AEE/IEEE/ISA			Remedial Class		Project Work	Remedial Class	

PE:AS-AVD,OS-VKS,ESD-AAG,DVD-ABG

Timetable Coordinator

HOD, E&C



Department of Electronics and Communication Engineering (2024 - 2025)

5th C Div

With Effect from 7/10/2024



Gogte Institute of Technology, Udyambag, Belgaum-590 008

AS-19

	1 10:00-10:55	2 10:55 - 11:50	B1 11:50-12:10	3 12:10-1:05	B2 1:05 - 2:00	4 2:00-2:55	5 3:00-5:00
Mo	M&E PSA	RMIPR UUD	BREAK	VLSI PUK	LUNCH	Project Work (2pm - 5pm)	
Tu	PE	VLSI PUK		DSP SM		(2pm-5pm) VLSI Lab-C1 (SMK/GN/MM) DSP Lab-C2 (SM/UUD/MBU)	
We	DSP SM	VLSI PUK		M&E PSA		BIZOTIC (2-4pm)	
Th	PE	DSP SM		RMIPR UUD		VLSI Lab-C2 (2-3pm) (SNB/PUK)	EVS (3pm-5pm)
Fr	PE	VLSI Lab-C1 (SMK/MM)		M&E PSA		(2pm-5pm) VLSI Lab-C2 (SNB/PUK/GN) DSP Lab-C1 (SM/VKA/UUD)	
Sa	AEE/IEEE/ISA			Remedial Class		Project Work	Remedial Class

PE:AS-AVD,OS-VKS,ESD-AAG,DVD-ABG

Timetable Coordinator

HOD, E&C