

# Confirmed Open Day Applied Students List of 5th Semester B.E. (All Branches) for Odd Semester 2022-23

**Academic Year** 2022-23  
**Degree** BE  
**Degree Batch** REGULAR  
**Department** ALL  
**Term** 5  
**Scheme** Scheme 2018  
**Examination** JANUARY\_MARCH\_2023 - REGULAR  
**Generated On** 29 Mar 2023 10:54:48 PM

Sl.No	USN	Name of Student	Subject Code	Subject Name	Open Day Status
1	2GI18EC147	Spoorti Yashodhar Kotian	18EC53	INFORMATION THEORY AND DIGITAL COMMUNICATION	VERIFIED
2	2GI20AE014	Kunal Shailesh Raikar	18AE54	GAS DYNAMICS	VERIFIED
3	2GI20CS013	Deshpande Anish Makarand	18CS51	COMPUTER NETWORKS	VERIFIED
4	2GI20CS013	Deshpande Anish Makarand	18CS52	OBJECT ORIENTED MODELING AND DESIGN	VERIFIED
5	2GI20CS016	Anshul Sonwalkar	18CS51	COMPUTER NETWORKS	VERIFIED
6	2GI20CS016	Anshul Sonwalkar	18CS52	OBJECT ORIENTED MODELING AND DESIGN	VERIFIED
7	2GI20CS018	Arbaaz M Khatib	18CS51	COMPUTER NETWORKS	VERIFIED
8	2GI20CS027	Ayman Ilyas Bagban	18CS51	COMPUTER NETWORKS	VERIFIED
9	2GI20CS027	Ayman Ilyas Bagban	18CS551	ADVANCED WEB PROGRAMMING	VERIFIED
10	2GI20CS028	Baldev P Benake	18CS51	COMPUTER NETWORKS	VERIFIED
11	2GI20CS031	Chidambar Ninganagoud Patil	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
12	2GI20CS040	Divyani Mahantesh Kumbar	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
13	2GI20CS043	Harsh Hemant Dalal	18CS52	OBJECT ORIENTED MODELING AND DESIGN	VERIFIED
14	2GI20CS046	Ishwari Mallikarjun Math	18INT54	DIGITAL MARKETING	VERIFIED
15	2GI20CS082	Paritosh Kumar	18CS51	COMPUTER NETWORKS	VERIFIED
16	2GI20CS082	Paritosh Kumar	18CS53	UNIX SYSTEM PROGRAMMING	VERIFIED
17	2GI20CS082	Paritosh Kumar	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED

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18	2GI20CS082	Paritosh Kumar	18INT55	FINANCE FOR ENGINEERS	VERIFIED
19	2GI20CS094	Prathamesh Barigheid	18CS53	UNIX SYSTEM PROGRAMMING	VERIFIED
20	2GI20CS099	Pritham Baswani Giryalkar	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
21	2GI20CS137	Shivam Arun Karamudi	18CS53	UNIX SYSTEM PROGRAMMING	VERIFIED
22	2GI20CS140	Shivani Vikas Banke	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
23	2GI20CV027	Manjunath	18CV54	DESIGN OF RCC STRUCTURAL ELEMENTS	VERIFIED
24	2GI20EC002	Abdulla Jawad Bawa	18EC53	INFORMATION THEORY AND DIGITAL COMMUNICATION	VERIFIED
25	2GI20EC017	Akshata Gangappa Kumbar	18CS565	OBJECT ORIENTED PROGRAMMING WITH C++	VERIFIED
26	2GI20EC055	Keerti Ravindra Ghorpade	18CS565	OBJECT ORIENTED PROGRAMMING WITH C++	VERIFIED
27	2GI20EC065	Mohinuddin S Kolar	18EC54	ENGINEERING ELECTROMAGNETICS	VERIFIED
28	2GI20EC094	Rajat Shrikant Sunadoli	18EC54	ENGINEERING ELECTROMAGNETICS	VERIFIED
29	2GI20EC141	Srushti B Patil	18EC54	ENGINEERING ELECTROMAGNETICS	VERIFIED
30	2GI20EC172	Anmol Nelavigi	18EC54	ENGINEERING ELECTROMAGNETICS	VERIFIED
31	2GI20EC172	Anmol Nelavigi	18EC556	ELECTRONIC SYSTEM DESIGN	VERIFIED
32	2GI20EC179	Amruta Adiraj Alagur	18EC51	OPERATING SYSTEM	VERIFIED
33	2GI20IS010	Clyde Joseph Dsouza	18INT55	FINANCE FOR ENGINEERS	VERIFIED
34	2GI20IS010	Clyde Joseph Dsouza	18IS52	OBJECT ORIENTED MODELING AND DESIGN	VERIFIED
35	2GI20IS038	Shashank G Joshi	18INT54	DIGITAL MARKETING	VERIFIED
36	2GI20IS038	Shashank G Joshi	18IS51	COMPUTER NETWORKS	VERIFIED
37	2GI20IS038	Shashank G Joshi	18IS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
38	2GI20IS041	Smruti Sateesh Bhandiwad	18IS52	OBJECT ORIENTED MODELING AND DESIGN	VERIFIED
39	2GI20IS041	Smruti Sateesh Bhandiwad	18IS53	INTERNET OF THINGS	VERIFIED
40	2GI20IS041	Smruti Sateesh Bhandiwad	18ME561	PRODUCT DESIGN AND RAPID PROTOTYPING	VERIFIED
41	2GI20ME009	Aishwarya S Panchaxarimath	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED
42	2GI20ME009	Aishwarya S Panchaxarimath	18ME53	DYNAMICS OF MACHINES	VERIFIED
43	2GI20ME015	Amin Ismayil Makandar	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED

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44	2GI20ME015	Amin Ismayil Makandar	18ME555	INDUSTRIAL ENGINEERING AND ERGONOMICS	VERIFIED
45	2GI20ME030	Jatin Anil Mulagund	18ME53	DYNAMICS OF MACHINES	VERIFIED
46	2GI20ME063	Shantanu Ramkrishna Hadgal	18CS565	OBJECT ORIENTED PROGRAMMING WITH C++	VERIFIED
47	2GI20ME063	Shantanu Ramkrishna Hadgal	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED
48	2GI20ME063	Shantanu Ramkrishna Hadgal	18ME52	APPLIED THERMODYNAMICS	VERIFIED
49	2GI20ME063	Shantanu Ramkrishna Hadgal	18ME53	DYNAMICS OF MACHINES	VERIFIED
50	2GI20ME067	Shrinidhi Vijay Joshi	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED
51	2GI20ME071	Sourabh Ravindra Lande	18ME52	APPLIED THERMODYNAMICS	VERIFIED
52	2GI20ME075	Swati Shivanand Nashipudi	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED
53	2GI20ME075	Swati Shivanand Nashipudi	18ME52	APPLIED THERMODYNAMICS	VERIFIED
54	2GI20ME083	Vinayak Shivakumar Shettar	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED
55	2GI20ME083	Vinayak Shivakumar Shettar	18ME53	DYNAMICS OF MACHINES	VERIFIED
56	2GI21CS405	Khushi Satish Patil	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
57	2GI21CS405	Khushi Satish Patil	18CS551	ADVANCED WEB PROGRAMMING	VERIFIED
58	2GI21CS413	Sangangouda B Patil	18CS52	OBJECT ORIENTED MODELING AND DESIGN	VERIFIED
59	2GI21CS413	Sangangouda B Patil	18CS53	UNIX SYSTEM PROGRAMMING	VERIFIED
60	2GI21CS413	Sangangouda B Patil	18CS54	FORMAL LANGUAGES AND AUTOMATA THEORY	VERIFIED
61	2GI21CV407	Aydin Kolhapuri	18CV53	ANALYSIS OF INDETERMINATE STRUCTURES	VERIFIED
62	2GI21CV407	Aydin Kolhapuri	18CV54	DESIGN OF RCC STRUCTURAL ELEMENTS	VERIFIED
63	2GI21CV407	Aydin Kolhapuri	18CVL58	DESIGN AND DRAWING OF HYDRAULIC STRUCTURES	VERIFIED
64	2GI21EC403	Chaytan S Hulmani	18CS565	OBJECT ORIENTED PROGRAMMING WITH C++	VERIFIED
65	2GI21EC412	Prajwal Jalagekar	18DMATEC51	PARTIAL DIFFERENTIAL EQUATIONS,Z-TRANSFORMS AND STOCHASTIC PROCESSES	VERIFIED
66	2GI21EC412	Prajwal Jalagekar	18EC52	CMOS VLSI DESIGN	VERIFIED
67	2GI21EC412	Prajwal Jalagekar	18EC551	POWER ELECTRONICS	VERIFIED
68	2GI21EC415	Radhika R Kalaghan	18EC54	ENGINEERING ELECTROMAGNETICS	VERIFIED
69	2GI21EC426	Sujay Gopivallabh Katti	18EC52	CMOS VLSI DESIGN	VERIFIED

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70	2GI21EC426	Sujay Gopivallabh Katti	18EC551	POWER ELECTRONICS	VERIFIED
71	2GI21ME488	SOURAV SHREENATH KUDALE	18ME51	DESIGN OF MACHINE ELEMENTS	VERIFIED
72	2GI21ME488	SOURAV SHREENATH KUDALE	18ME53	DYNAMICS OF MACHINES	VERIFIED

Date: 30.03.2023



A handwritten signature in blue ink, appearing to read "Shravanji".

Controller of Examinations  
**CONTROLLER OF EXAMINATIONS**  
Karnatak Law Society's  
Gogte Institute of Technology  
Udyambag, BELAGAVI - 590 008.  
Karnataka